

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

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## Title of Invention

STRUCTURES AND METHODS FOR MANUFACTURING OF  
DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON  
AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING  
WITH SiGe AND/OR Si:C

Submission Type : Utility Patent Filing

Application Number:

10/709239



EFS ID:

59509

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the function call.

First Named Applicant: Huilong Zhu

Attorney Docket Number: FIS920030375

Timestamp: 2004-04-23 09:16:27 EDT

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File Listing:

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us-request	FIS920030375US1-usrequ.xml	3954	2004-04-23
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us-assignment	FIS920030375US1-usassn.xml	2092	2004-04-23
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us-assignment	us-assignment.dtd	10898	2004-04-23
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us-assignment	FIS920030375assign2.tif	11320	2004-04-23
us-fee-sheet	FIS920030375US1-usfees.xml	2100	2004-04-23
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us-declaration	FIS920030375dec2.tif	19654	2004-04-23
application-body	FIS920030375US1-trans.xml	43106	2004-04-23
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application-body	application-body.dtd	49498	2004-04-23
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